

```
bb.0.main_body:
successors: %bb.1(0x40000000), %bb.2(0x40000000); %bb.1(50.00%), %bb.2(50.00%)
liveins: $vgpr0, $vgpr1
%5:vgpr_32 = COPY killed $vgpr1
%4:vgpr_32 = COPY killed $vgpr0
%0:vgpr_32 = nofpexcept V_MUL_F32_e64_0, 1077936128, 0, %5:vgpr_32, 0, 0, implicit $mode, implicit $exec
%8:sreg_32 = V_CMP_LT_I32_e64_5, killed %4:vgpr_32, implicit $exec
%9:vgpr_32 = COPY killed %0:vgpr_32
%1:sreg_32 = SI_IF killed %8:sreg_32, %bb.2, implicit-def dead $exec, implicit-def dead $scc, implicit $exec
S_BRANCH %bb.1
```

```
bb.1.if:
; predecessors: %bb.0
successors: %bb.2(0x80000000); %bb.2(100.00%)

%2:vgpr_32 = nofpexcept V_ADD_F32_e64_0, killed %5:vgpr_32, 0, %5:vgpr_32, 0, 0, implicit $mode, implicit $exec
%9:vgpr_32 = COPY killed %2:vgpr_32
SI_WAVE_RECONVERGE killed %1:sreg_32, implicit-def dead $exec, implicit-def dead $scc, implicit $exec
```

```
bb.2.end:
; predecessors: %bb.0, %bb.1

%3:vgpr_32 = COPY killed %9:vgpr_32
$vgpr0 = COPY killed %3:vgpr_32
SI_RETURN_TO_EPILOG killed $vgpr0
```

CFG for 'else_no_execfix' function